

MICROELECTRÓNICA
Exercício 3 - Layout do MOSFET
Layout Hierárquico de um Pad Frame

Problema 1 - Faça o layout de um transistor NMOS com $W=L=5 \mu\text{m}$

Problema 2 - Faça o layout de um transistor PMOS com $W=10 \mu\text{m}$,
 $L=2 \mu\text{m}$

Problema 3 - Faça o layout de um transistor PMOS com $W=200 \mu\text{m}$,
 $L=2 \mu\text{m}$. Utilize 4 transistores em paralelo - estrutura em pente (*finger structure*) - como se mostra na figura ao lado

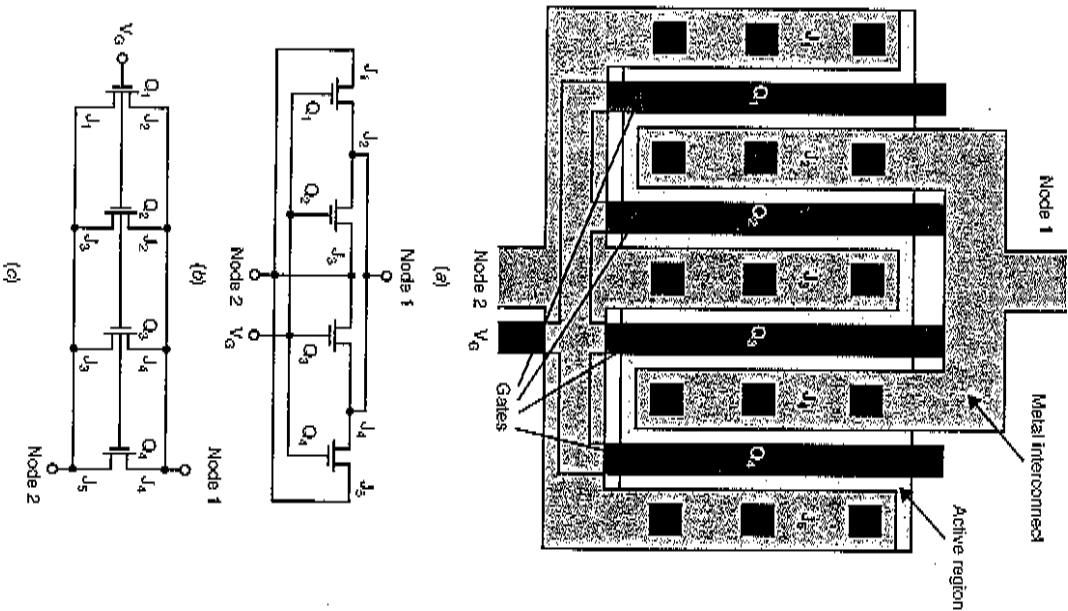


Fig. 2.17 Connecting four transistors in parallel to realize a single large transistor: (a) the layout, (b) the schematic drawn in the same relative positions as the layout, and (c) the circuit redrawn to make the parallel transistors more obvious.