

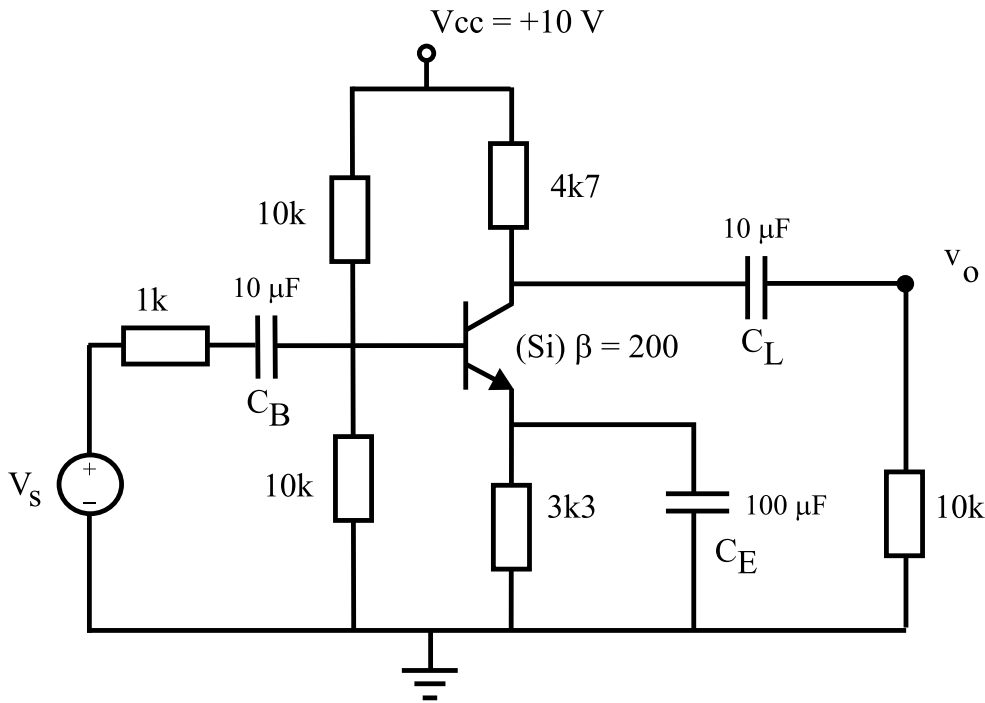
Trabalho 2. Theoretical Values

P. Stallinga

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1: COMMON EMITTER

Assume: $\beta = 200$ ($\alpha = 1$), $V_A = 120$ V, $C_\mu = 8$ pF, $C_\pi = 25$ pF



Actual values:

$R_S = 1 \text{ k}\Omega$
 $C_S = 10 \text{ }\mu\text{F}$
 $R_{B1} = 10 \text{ k}\Omega$
 $R_{B2} = 20 \text{ k}\Omega$
 $R_C = 4.7 \text{ k}\Omega$
 $C_L = 10 \text{ }\mu\text{F}$
 $R_E = 3.3 \text{ k}\Omega$
 $C_E = 100 \text{ }\mu\text{F}$
 $R_L = 10 \text{ k}\Omega$
 $V_{CC} = +10 \text{ V}$

Bias conditions:

$$V_B = +10 \text{ V} \cdot 10 \text{ k}\Omega / (20 \text{ k}\Omega + 10 \text{ k}\Omega) = 3.33 \text{ V}$$

$$V_E = V_B - 0.7 \text{ V} = 2.63 \text{ V}$$

$$I_E = 2.63 \text{ V} / 3.3 \text{ k}\Omega = 0.797 \text{ mA}$$

$$V_C = 10 - 0.797 \cdot 10^{-3} \cdot 4700 = 6.25 \text{ V}$$

$$r_e = 26 \text{ mV} / 0.798 \text{ mA} = 32.58 \text{ }\Omega$$

$$r_\pi = (\beta + 1)r_e = 6.5449 \text{ k}\Omega$$

$$r_o = 120 \text{ V} / 0.798 \text{ mA} = 150 \text{ k}\Omega$$

$$g_m = 1/r_e = 30.7 \text{ mS}$$

Gain:

The gain is defined by the standard formula for a CE amplifier:

$$A_v = -\frac{R_C}{R_E + r_e}$$

In the intermediate frequency regime all the condensators (of the LPFs) can be considered short-circuited. Thus, the resistor R_E is bypassed and the gain increases to

$$A_v = -\frac{R_C}{r_e} = -\frac{4700}{32.58} = -144.26$$

To calculate the input resistance of the transistor and the stage we also consider R_E bypassed:

$$r_{in}(\text{transistor}) = (\beta + 1)r_e = 201 \cdot 32.58 \Omega = 6.549 \text{ k}\Omega$$

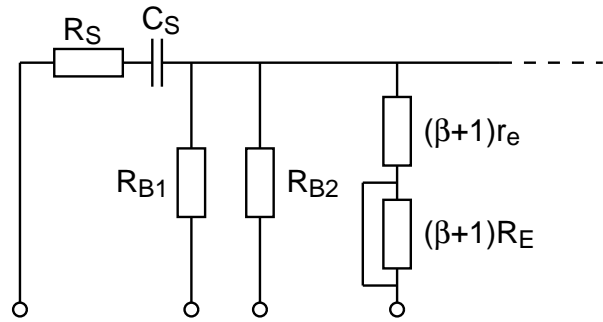
$$\begin{aligned} r_{in}(\text{stage}) &= R_{B1} \parallel R_{B2} \parallel r_{in} \\ &= 10 \text{ k}\Omega \parallel 20 \text{ k}\Omega \parallel 6.549 \text{ k}\Omega = 3.304 \text{ k}\Omega \end{aligned}$$

$$\begin{aligned} \frac{v_o}{v_i} &= A_v \left[\frac{r_{in}(\text{stage})}{r_S + r_{in}(\text{stage})} \right] \left[\frac{R_L}{R_L + R_C} \right] \\ &= -144.26 \left(\frac{3.304}{1 + 3.304} \right) \left(\frac{10}{10 + 4.7} \right) \\ &= -144.26 \cdot 0.7677 \cdot 0.6803 = -75.33 \end{aligned}$$

lower cut-off frequency:

Using the short-circuit-time-constants method (all other C's are short circuited)

1: The condensator (C_S) at the input is part of a high-pass filter (HPF). To calculate the cut-off frequency of this filter we have to calculate the RC time and hence we have to determine the resistance this condensator sees. Figure 2 summarizes this. The part after the collector can be neglected, because it is much larger. The resistor R_E is bypassed by the capacitor C_E .



$$\begin{aligned} R &= R_S + R_{B1} \parallel R_{B2} \parallel (\beta + 1)r_e \\ &= 1\text{k}\Omega + 10 \text{ k}\Omega \parallel 20 \text{ k}\Omega \parallel 6.549 \text{ k}\Omega \\ &= 4.304 \text{ k}\Omega \end{aligned}$$

$$C_S = 10 \mu\text{F}$$

$$\tau_S = RC = 43.04 \text{ ms}$$

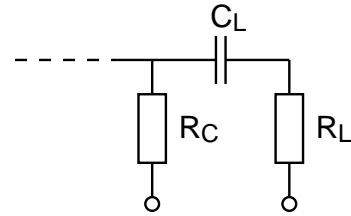
$$f_{LS} = \frac{1}{2\pi\tau_S} = 3.7 \text{ Hz}$$

2: For the condenser at the load we have:

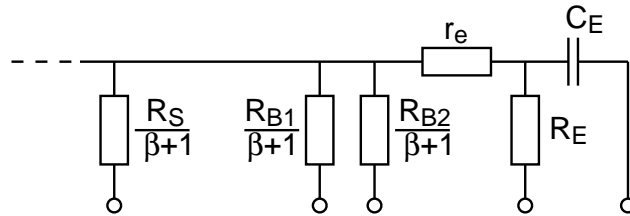
$$R = R_L + R_C = 10 \text{ k}\Omega + 4.7 \text{ k}\Omega = 14.7 \text{ k}\Omega$$

(note: the resistance after the collector is again neglected). With the capacitance $C = C_L = 10 \text{ }\mu\text{F}$ we have

$$\begin{aligned} \tau_L &= RC = 147 \text{ ms} \\ f_{LL} &= \frac{1}{2\pi\tau_L} = 1.08 \text{ Hz} \end{aligned}$$



3: Finally, for the emitter condenser we have (everything at the collector ignored):



$$\begin{aligned} R &= R_E \parallel \left(r_e + \frac{R_{B1}}{\beta + 1} \parallel \frac{R_{B2}}{\beta + 1} \parallel \frac{R_S}{\beta + 1} \right) \\ &= 3300 \text{ }\Omega \parallel (32.58 \text{ }\Omega + 49.75 \text{ }\Omega \parallel 99.50 \text{ }\Omega \parallel 4.975 \text{ }\Omega) = 36.5 \text{ }\Omega \\ C &= C_E = 100 \text{ }\mu\text{F} \\ \tau_E &= RC = 3.65 \text{ ms} \\ f_{LE} &= \frac{1}{2\pi\tau_E} = 43 \text{ Hz} \end{aligned}$$

The cut-off frequency (f_L) is determined by the higher of the three f_{LS} , f_{LL} and f_{LE} , namely by condenser at the emitter, $f_L = f_{LE}$. More exact, $f_L = f_{LS} + f_{LL} + f_{LE} = 3.7 \text{ Hz} + 1.08 \text{ Hz} + 43 \text{ Hz} = 48 \text{ Hz}$. Below this frequency the attenuation is 20 dB / decade. The phase slowly shifts 90° and is 45° at f_L . When the frequency is further lowered it hits the second cut-off frequency (approximately f_{LS}) from where the attenuation will be 40 dB / decade, etc. Each filter introduces an attenuation of 20 dB / decade and a phase shift of 90°.

higher cut-off frequency:

Using the open-circuit-time-constants method (all other high frequency C's are open circuit). The higher cut-off frequencies are determined by the shunt capacitances that connect the input or the output to ground. We have C_π (between base and emitter) and C_μ (between base and collector). Note that in this configuration the latter is connected between the input and the output (with negative gain) and is therefore susceptible to feedback effects (resulting in so-called Miller capacitance, C_M).

For the higher cut-off frequencies it is most convenient to decompose the system into an input filter and an output filter. This is allowed because the resistance between the input and the output is so high that there is virtually no contact between these points. At the input we have:

$$\begin{aligned} R &= R_S \parallel R_{B1} \parallel R_{B2} \parallel (\beta + 1)r_e \\ &= 1 \text{ k}\Omega \parallel 10 \text{ k}\Omega \parallel 20 \text{ k}\Omega \parallel 6.549 \text{ k}\Omega \end{aligned}$$

$$\begin{aligned}
&= 767.6 \Omega \\
C &= C_\pi + C_M = C_\pi + C_\mu(1 - A_v) \\
&= 25 \text{ pF} + 8 \text{ pF}(1 + 98.14) \\
&= 818.12 \text{ pF} \\
\tau_i &= RC = 627.99 \text{ ns} \\
f_{Hi} &= \frac{1}{2\pi\tau_i} = 253 \text{ kHz}
\end{aligned} \tag{1}$$

Note that for the amplification A_v we have to use the voltage amplification factor between the collector and the base because the condenser C_μ is connected between those points. It does not include the voltage divider at the source:

$$A_v = -\frac{R_C \parallel R_L}{r_e} = -98.14$$

At the output we have:

$$\begin{aligned}
R &= r_c \parallel R_C \parallel R_L \\
&= 150 \text{ k}\Omega \parallel 4.7 \text{ k}\Omega \parallel 10 \text{ k}\Omega \\
&= 3.131 \text{ k}\Omega \\
C &= C_\mu(1 - 1/A_v) \\
&= 8 \text{ pF} \cdot 1.0102 \\
&= 8.08 \text{ pF} \\
\tau_o &= RC = 25.3 \text{ ns} \\
f_{Ho} &= \frac{1}{2\pi\tau_o} = 6.3 \text{ MHz}
\end{aligned}$$

The cut-off frequency is therefore determined by the filter at the input. Using the open-circuit-time-constants method $\tau = \tau_i + \tau_o = 627.99 \text{ ns} + 25.3 \text{ ns} = 655 \text{ ns}$ and $f_H = 1/2\pi\tau = 243 \text{ kHz}$.

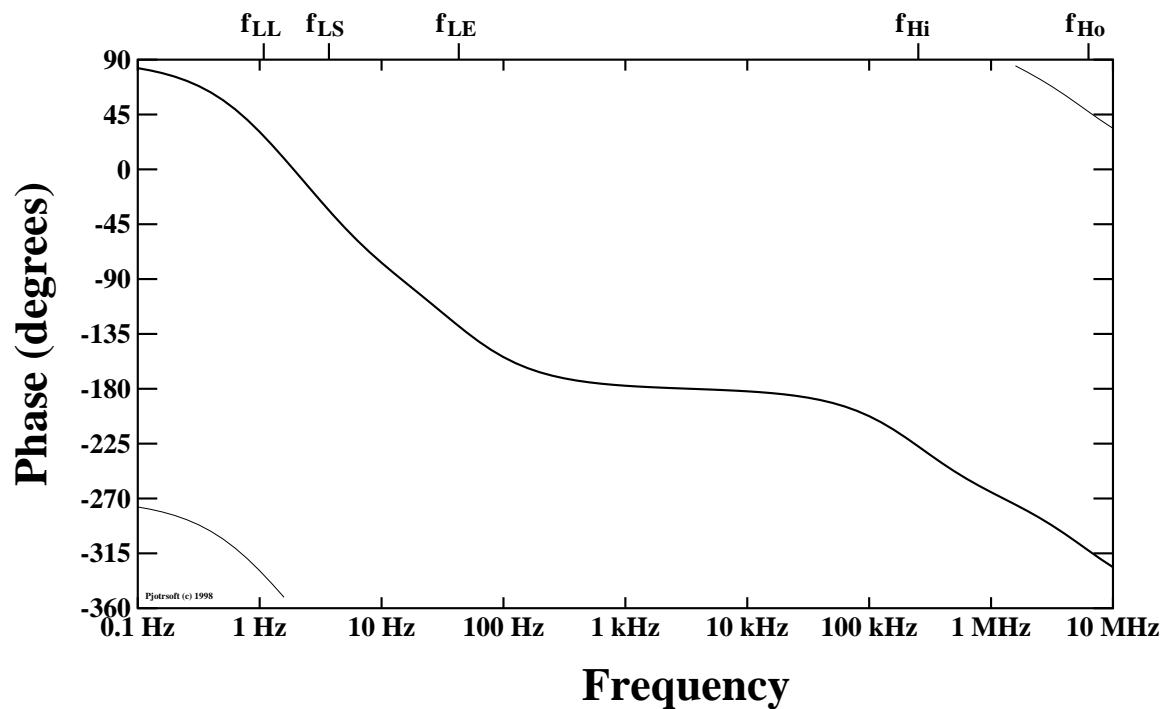
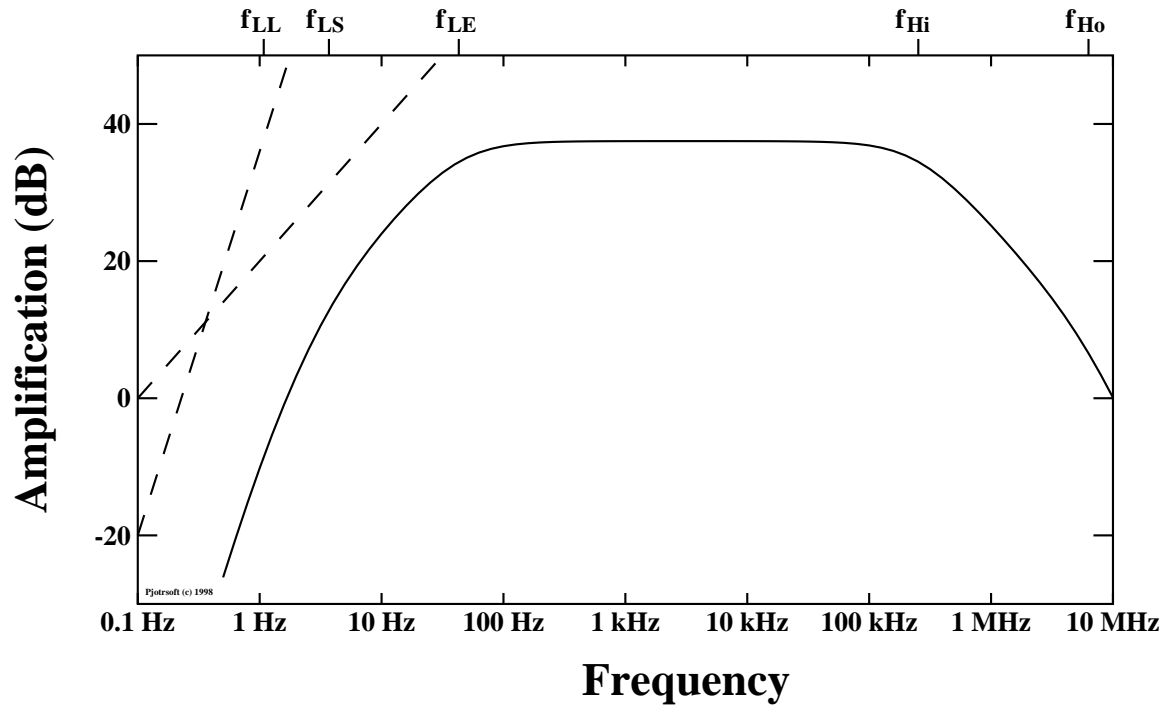
Finally, the internal cut-off frequency of the transistor is determined by (see p. 437 of Bogart)

$$f_T = \frac{1}{2\pi r_e (C_\pi + C_\mu)} = \frac{1}{2\pi(32.58 \Omega)(12 \text{ pF})} = 407 \text{ MHz}$$

(According to the specifications (datasheets), for the 2N2222 the unitary current amplification frequency is $f_T = 250 \text{ MHz}$.) This cut-off frequency is not important for the circuit. The gain and cut-off frequencies are summarized in the Bode plot on the next page.

phase diagram:

The overall gain of the amplifier at mid-band frequencies is negative. This is equal to a phase shift of 180° . The filter at the source with f_{LS} introduces a phase shift of $+90^\circ$ (see p. 33 of Sedra) at low frequencies. In fact, every HPF introduces a phase shift of 90° . On the other side of the spectrum, every LPF introduces a phase shift of -90° at high frequencies. The figures show the voltage gain (in dB) at the load and the phase relative to the source voltage:



Without C_E :

When the shunt capacitance C_E is pulled, the gain of the amplifier reduces to

$$A_v = -\frac{R_C}{r_e + R_E} = -\frac{4700}{32.58 + 3300} = -1.410$$

The voltage gain between the base and the collector (to which C_μ is connected; we will use this

value to calculate the Miller capacitance) when a load resistance is connected is

$$A_v = -1.410 \cdot \frac{R_L}{R_L + R_C} = -1.410 \cdot \frac{10}{10 + 4.7} = -0.959$$

The voltage divider at the input:

$$\begin{aligned} r_{in} &= (\beta + 1)(r_e + R_E) = 669.8 \text{ k}\Omega \\ (r_{in})_{\text{stage}} &= r_{in} \parallel R_{B1} \parallel R_{B2} = 669.8 \text{ k}\Omega \parallel 10 \text{ k}\Omega \parallel 20 \text{ k}\Omega = 6.6 \text{ k}\Omega \\ \frac{v_i}{v_S} &= \frac{(r_{in})_{\text{stage}}}{(r_{in})_{\text{stage}} + R_S} = \frac{6.6}{6.6 + 1} = 0.868 \end{aligned}$$

The ratio of the voltage at the output and the voltage at the input then becomes (taking into account the voltage dividers at the input and the output):

$$\frac{v_o}{v_i} = -0.832$$

The filter at the emitter is removed and the highest cut-off frequency is then $f_1(S)$ whose value is also slightly changed by the elimination of the shunt capacitance:

$$\begin{aligned} R &= R_S + R_{B1} \parallel R_{B2} \parallel (\beta + 1)(r_e + R_E) \\ &= 7.601 \text{ k}\Omega \\ C &= 10 \text{ }\mu\text{F} \\ \tau_S &= RC = 76.01 \text{ ms} \\ f_{LS} &= \frac{1}{2\pi\tau_S} = 2.09 \text{ Hz} \\ f_{LL} &= f_{LL} = 1.08 \text{ Hz} \end{aligned}$$

For the higher cut-off frequency.

1: At the entrance of the transistor:

$$\begin{aligned} R &= R_S \parallel R_{B1} \parallel R_{B2} \parallel (\beta + 1)(R_E + r_e) \\ &= 1 \text{ k}\Omega \parallel 10 \text{ k}\Omega \parallel 20 \text{ k}\Omega \parallel 669.8 \text{ k}\Omega = 868 \text{ }\Omega \\ C &= C_\pi + (1 - A_v)C_\mu = 25 \text{ pF} + (1 + 0.959) \cdot 8 \text{ pF} = 40.7 \text{ pF} \\ \tau_i &= RC = 35.3 \text{ ns} \\ f_{Hi} &= \frac{1}{2\pi\tau_i} = 4.5 \text{ MHz} \end{aligned}$$

2: At the exit of the transistor:

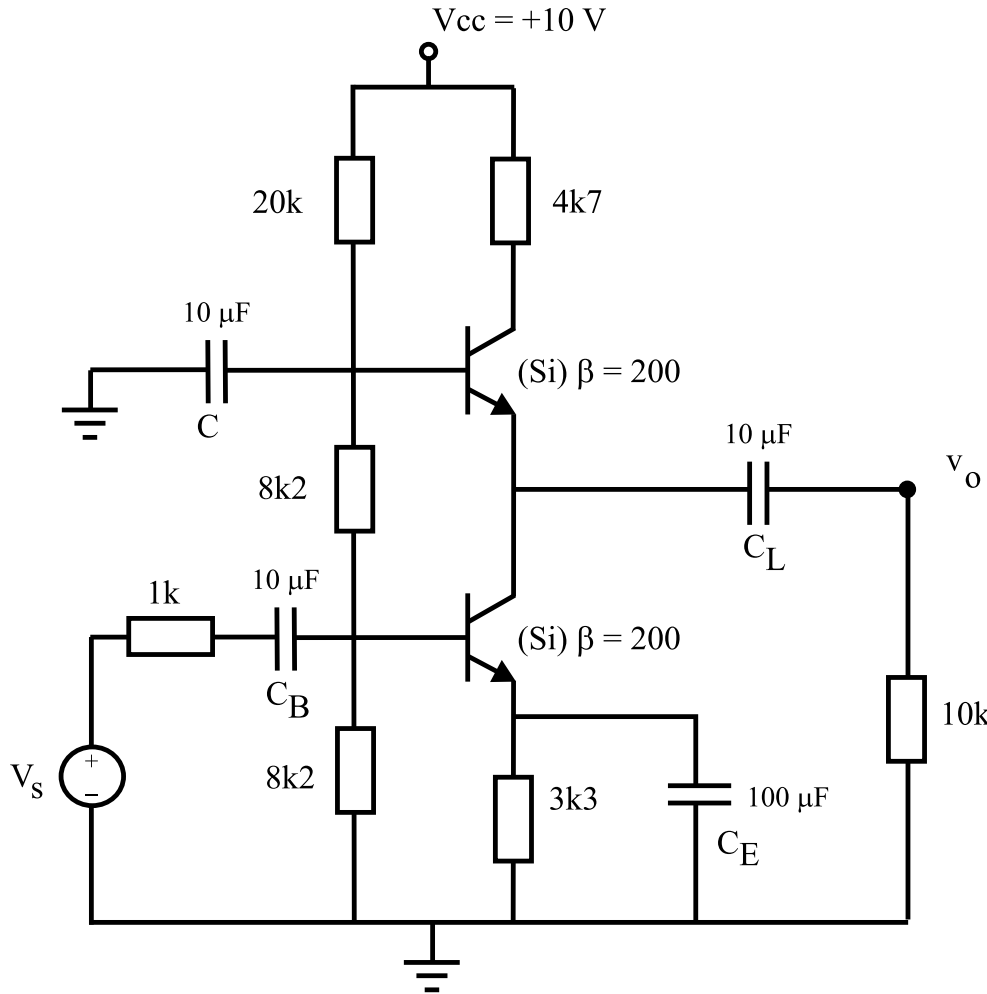
$$\begin{aligned} R &= R_L \parallel R_C = 3.131 \text{ k}\Omega \\ C &= (1 - 1/A_v)C_\mu = (1 + 1/0.959) \cdot 8 \text{ pF} = 16.3 \text{ pF} \\ \tau_o &= RC = 51.0 \text{ ns} \\ f_{Ho} &= \frac{1}{2\pi\tau_o} = 3.12 \text{ MHz} \end{aligned}$$

3: internal filter:

$$f'_T = f_T = 407 \text{ MHz}$$

The cut-off frequency is determined by the exit of the transistor.

2: CASCODE



Actual values:

$R_S = 1 \text{ k}\Omega$
 $C_S = 10 \text{ }\mu\text{F}$
 $R_{B1} = 8.2 \text{ k}\Omega$
 $R_{B2} = 8.2 \text{ k}\Omega$
 $R_{B3} = 20 \text{ k}\Omega$
 $R_C = 4.7 \text{ k}\Omega$
 $C_L = 10 \text{ }\mu\text{F}$
 $R_E = 3.3 \text{ k}\Omega$
 $C_E = 100 \text{ }\mu\text{F}$
 $R_L = 10 \text{ k}\Omega$
 $V_{CC} = 15 \text{ V}$
 $C_B = 10 \text{ }\mu\text{F}$

Bias conditions:

$$V_{B1} = +15 \text{ V} \cdot 8.2 \text{ k}\Omega / (20 \text{ k}\Omega + 8.2 \text{ k}\Omega + 8.2 \text{ k}\Omega) = 3.38 \text{ V}$$

$$V_{E1} = V_B - 0.7 \text{ V} = 2.68 \text{ V}$$

$$I_{E1} = 2.68 \text{ V} / 3.3 \text{ k}\Omega = 0.8119 \text{ mA} \quad (3.3 \text{ k}\Omega \text{ chosen so that } I_{E1} \text{ will be approximately } 0.8 \text{ mA})$$

$$I_{C2} = I_{E2} = I_{C1} = I_{E1} \quad (\alpha=1)$$

$$V_{C2} = +15 - R_C I_{C2} = 11.18 \text{ V}$$

$$r_e = 26 \text{ mV} / 0.8119 \text{ mA} = 32.024 \text{ }\Omega$$

$$g_m = 1/r_e = 31.2 \text{ mS}$$

$$r_\pi = (\beta + 1)r_e = 6.437 \text{ k}\Omega$$

$$r_o = 120 \text{ V} / 0.8119 \text{ mA} = 150 \text{ k}\Omega$$

The output resistance of Q1 is r_o . The output resistance of Q2 is much higher (reasoning is similar to current source of TP1)

Gain:

$$\begin{aligned}
 (r_{in})_{\text{stage}} &= (\beta + 1)r_{e1} \parallel R_{B1} \parallel R_{B2} \\
 &= 6.437 \text{ k}\Omega \parallel 8.2 \text{ k}\Omega \parallel 8.2 \text{ k}\Omega \\
 &= 2.505 \text{ k}\Omega
 \end{aligned}$$

$$\begin{aligned}
(r_o)_{\text{stage}} &= R_C \parallel r_{\text{out},2} = R_C \\
&= 4.7 \text{ k}\Omega \\
A_v^1 &= -\frac{r_{\text{out},1} \parallel r_{e2}}{r_{e1}} = -\frac{r_{e2}}{r_{e1}} = -1 \\
A_v^2 &= \frac{R_C \parallel r_{\text{out},2}}{r_{e2} \parallel r_{c1}} = \frac{R_C}{r_{e2}} = \frac{4700}{32.024} = +147.76 \\
A_v &= A_v^1 \cdot A_v^2 = -1 \cdot 147.76 = -147.76 \\
\frac{v_L}{v_S} &= \frac{r_{\text{in}}}{r_{\text{in}} + r_S} \cdot A_v \cdot \frac{r_L}{r_L + r_o} \\
&= \left(\frac{2505}{1000 + 2505} \right) (-146.76) \left(\frac{10000}{10000 + 4700} \right) \\
&= 0.7147 \cdot (-146.764) \cdot 0.6803 \\
&= -71.36
\end{aligned}$$

At low frequencies nothing much changes, though there is an extra filter caused by the condensator C_B :

$$\begin{aligned}
C &= C_S = 10 \text{ }\mu\text{F} \\
R &= R_S + R_{B1} \parallel R_{B2} \parallel (\beta + 1)r_e = 1 + 8.2 \parallel 8.2 \parallel 6.437 = 3.505 \text{ k}\Omega \\
\tau_S &= RC = 35.05 \text{ ms} \\
f_{LS} &= 4.54 \text{ Hz} \\
\\
C &= C_B = 10 \text{ }\mu\text{F} \\
R &= R_{B3} \parallel (R_{B2} + R_S \parallel R_{B1} \parallel (\beta + 1)r_e) = 20 \parallel (8.2 + 1 \parallel 8.2 \parallel 6.437) = 6.199 \text{ k}\Omega \\
\tau_B &= RC = 61.99 \text{ ms} \\
f_{LB} &= 2.57 \text{ Hz} \\
\\
C &= C_E = 100 \text{ }\mu\text{F} \\
R &= R_E \parallel \left(r_e + \frac{R_{B1} \parallel R_{B2} \parallel R_S}{\beta + 1} \right) = 3.3 \parallel \left(0.032024 + \frac{8.2 \parallel 8.2 \parallel 1}{201} \right) = 35.63 \text{ }\Omega \\
\tau_E &= RC = 3.563 \text{ ms} \\
f_{LE} &= 44.67 \text{ Hz} \\
\\
C &= C_L = 10 \text{ }\mu\text{F} \\
R &= R_C + R_L = 4.7 + 10 = 14.7 \text{ k}\Omega \\
\tau_L &= RC = 147 \text{ ms} \\
f_{LL} &= 1.08 \text{ Hz}
\end{aligned}$$

The cut-off frequency is the largest of the four: 45 Hz. Or using the short-circuit-time-constants theory: $f_L = \sum_i f_i = 53 \text{ Hz}$.

The advantage is in the high frequencies, namely the virtual elimination of the Miller capacitance because the capacitance C_μ at the entrance of the stage is connected to two points with a voltage gain close to -1 as described above. The Miller capacitance therefore reduces to

$$C_M = C_\mu (1 - A_v) = 8 \text{ pF} (1 - (-1)) = 16 \text{ pF}$$

and the total capacitance becomes

$$C = C_{\pi} + C_M = 25 \text{ pF} + 16 \text{ pF} = 41 \text{ pF}$$

The resistance is approximately the same as in Equation 1.

$$\begin{aligned} R &= R_S \parallel R_{B1} \parallel R_{B2} \parallel (\beta + 1)r_e = 1 \parallel 8.2 \parallel 8.2 \parallel 6.537 = 714.7 \Omega \\ \tau_i = RC &= 29.3 \text{ ns} \\ f_{Hi} &= 5.43 \text{ MHz} \end{aligned}$$

At the output the capacitor C_{μ} now has one leg connected to ground (V_{B2}) and the Miller effect disappears ($1-0 = 1$)

$$\begin{aligned} R &= R_C \parallel R_L = 10 \parallel 4.7 = 3.197 \text{ k}\Omega \\ C &= C_{\mu} = 8 \text{ pF} \\ \tau &= RC = 25.6 \text{ ns} \\ f_{Ho} &= 6.2 \text{ MHz} \end{aligned}$$

In-between the two transistors we have another filter:

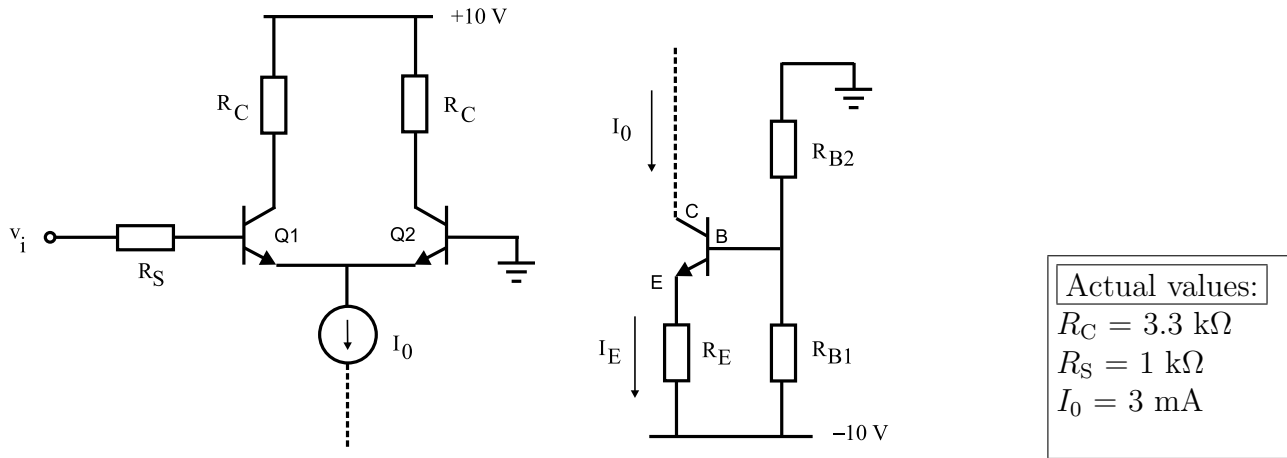
$$\begin{aligned} C &= C_{\pi 2} + (1 - (-1/1))C_{\mu 1} = 41 \text{ pF} \\ R &= r_e = 32.58 \Omega \\ \tau &= RC = 1.34 \text{ ns} \\ f_{Hm} &= 119 \text{ MHz} \end{aligned}$$

Using the theory of open-circuit time constants we find a total time constant of

$$\begin{aligned} \tau &= 29.3 \text{ ns} + 25.6 \text{ ns} + 1.34 \text{ ns} = 56.2 \text{ ns} \\ f_H &= 1/2\pi\tau = 2.8 \text{ MHz} \end{aligned}$$

Conclusion: there is a significant increase in high-frequency response by approximately one decade, basically caused by the elimination of the Miller effect at the entrance.

3: DIFFERENTIAL PAIR WITH CURRENT SOURCE



bias:

$I_0 = 3 \text{ mA}$, $I_E = 1.5 \text{ mA}$. $r_e = 26 \text{ mV} / 1.5 \text{ mA} = 17.33 \Omega$. $r_\pi = 201 \times 17.3 \Omega = 3.48 \text{ k}\Omega$. $V_{o2} = V_{o1} = 10 \text{ V} - 1.5 \text{ mA} \times 3.3 \text{ k}\Omega = 5.05 \text{ V}$. $r_{in} = 2 \times r_\pi = 7.0 \text{ k}\Omega$.

gain:

$$A_v^l = -\frac{\alpha R_C}{2r_e} = -95.4$$

$$A_v^r = +95.4$$

$$\frac{v_o}{v_s} = \frac{r_{in}}{r_{in} + R_S} A_v = \frac{(\beta + 1)2r_e}{(\beta + 1)2r_e + R_S} A_v = 0.8742 \cdot 95.2 = +83.4$$

high-frequencies:

The high-frequency response is not improved compared to a single-stage amplifier. At the input exists $C_\pi = 25 \text{ pF}$ and $C_\mu = 8 \text{ pF}$. Both suffer from the Miller effect and are therefore multiplied by the respective voltage gains between the two points that are bridged by the capacitance. From Trabalho 1 we know that in differential mode, the gain between base and collector of the left transistor (Q1) is given by

$$A_v = -\frac{\alpha R_C}{2r_e}$$

$$= -\frac{1 \cdot 3300}{2 \cdot 17.33}$$

$$= -95.2$$

The gain felt by C_π is exactly 0.5. The total capacitance at the input is therefore

$$C = (1 - 0.5)C_\pi + C_M = 0.5C_\pi + C_\mu(1 - A_v)$$

$$= 12.5 \text{ pF} + 8 \text{ pF}(1 - (-95.4)) = 783.7 \text{ pF}$$

The resistance-to-ground at the input is

$$R = R_S || (\beta + 1)2r_e$$

$$\begin{aligned}
&= 1 \text{ k}\Omega \parallel 201 \cdot 17.33 \text{ }\Omega = 777 \text{ }\Omega \\
\tau_i &= RC = 608.9 \text{ ns} \\
f_{Hi} &= \frac{1}{2\pi\tau_i} = 261 \text{ kHz}
\end{aligned}$$

At the output (collector of right transistor Q2) we have $C_\mu = 8 \text{ pF}$ to the base. Without any Miller effects, because the gain between emitter and base is zero. The total capacitance is therefore

$$C = C_\mu = 8 \text{ pF}$$

At the output the resistance is $R_C \parallel r_{\text{out},2} \approx R_C = 3.3 \text{ k}\Omega$. Therefore

$$\begin{aligned}
\tau_o &= RC = 26.4 \text{ ns} \\
f_{Ho} &= \frac{1}{2\pi\tau_o} = 6.0 \text{ MHz}
\end{aligned}$$

Note: with a load resistance of $10 \text{ k}\Omega$, the equivalent resistance at the output would be $2.48 \text{ k}\Omega$ and the frequency would further increase to 8.0 MHz .

The internal cut-off frequency remains at

$$f_T = f_T = 407 \text{ MHz}$$

low frequencies:

Since there are no coupling capacitors at the entrance or the exit, there is no theoretical lower cut-off frequency. If we *do* introduce them (at the source and the load only) we can say the following: Since there is no need for any bypass condensator at the emitter we can expect an improvement of the lower frequency response, since in the single-stage amplifiers the lower-frequency response is limited by this condensator (see first part). There still remain the filters at the source and at the load. For the source we have $C_S = 10 \text{ }\mu\text{F}$ and the resistance is here $R_S + (\beta + 1)2r_e = 4.48 \text{ k}\Omega$. The cut-off frequency is then $f_{LS} = 3.6 \text{ Hz}$. At the load the resistance is $R_L + R_C \parallel r_o \approx 4.3 \text{ k}\Omega$ and with a load-condensator of $10 \text{ }\mu\text{F}$ this gives a cut-off frequency of $f_{LL} = 3.7 \text{ Hz}$.

In conclusion: For a differential pair we see a significant improvement of the low-frequency performance compared to a single-stage amplifier (about one decade), mainly due to the elimination of the emitter condensator.

FURTHER INFORMATION:

Chapter 10 of "Electronic Devices and Circuits", T. F. Bogart, Jr., 4th edition, Prentice Hall, 1997.

Chapter 7 of "Microelectronic Circuits", A. S. Sedra, K. C. Smith, 2nd edition, Oxford University Press, 1982.